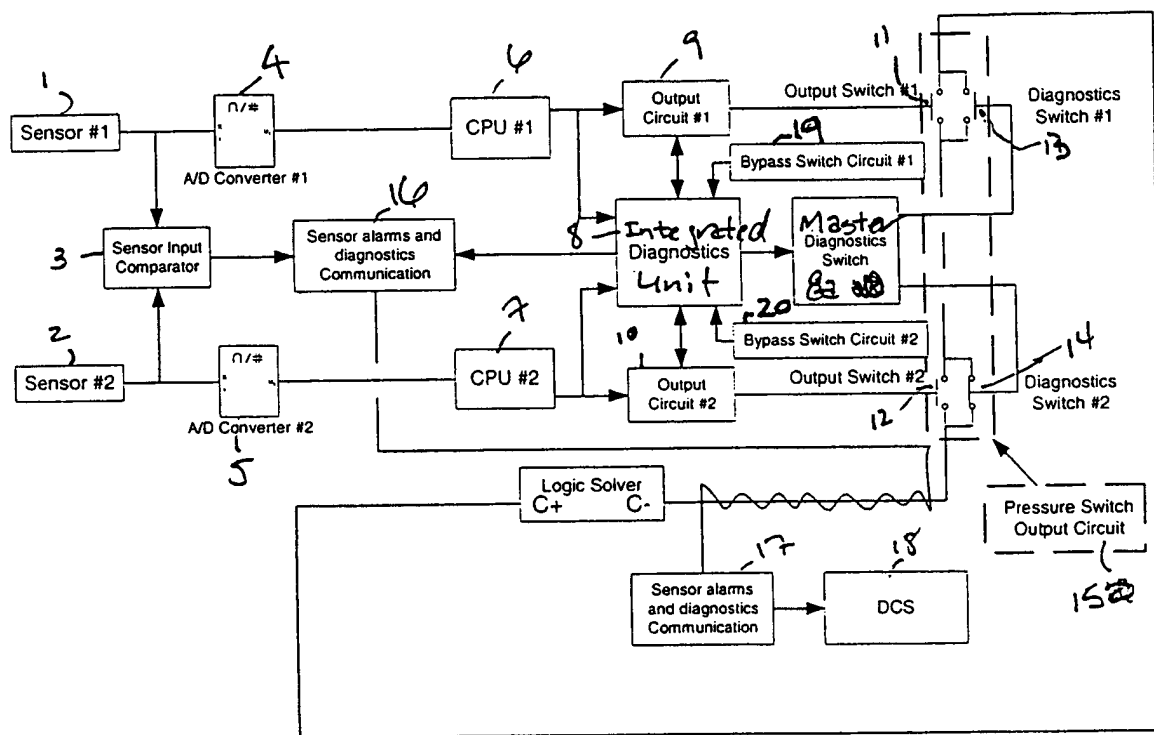
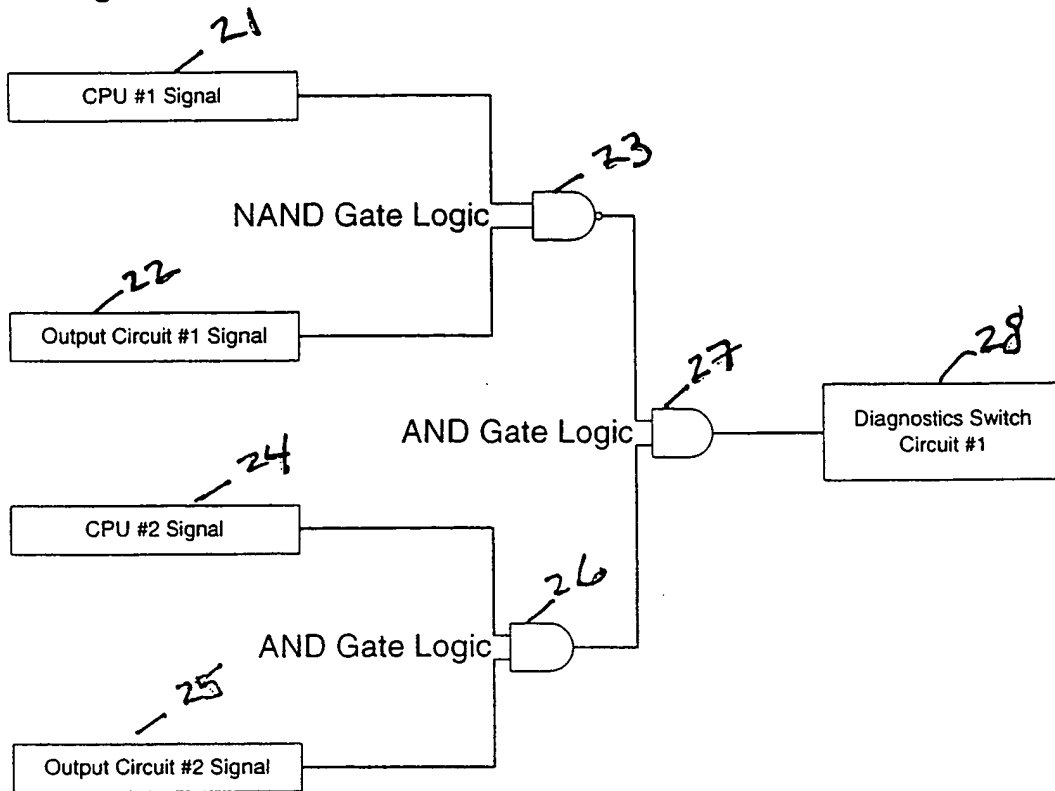


Figure 1



**Figure 2**



**Figure 3**

| Output Circuit | Diagnostics Switch Circuit #1 | CPU #1 Signal | Output Circuit #1 Signal | CPU #2 Signal | Output Circuit #2 Signal |
|----------------|-------------------------------|---------------|--------------------------|---------------|--------------------------|
| Open           | Open                          | 0             | 0                        | 0             | 0                        |
| Open           | Open                          | 0             | 0                        | 1             | 0                        |
| Open           | Open                          | 0             | 0                        | 0             | 1                        |
| Open           | Open                          | 0             | 0                        | 1             | 1                        |
| Open           | Open                          | 1             | 0                        | 0             | 0                        |
| Open           | Open                          | 1             | 0                        | 1             | 0                        |
| Open           | Open                          | 1             | 0                        | 0             | 1                        |
| Closed         | Closed                        | 1             | 0                        | 1             | 1                        |
| Open           | Open                          | 0             | 1                        | 0             | 0                        |
| Open           | Open                          | 0             | 1                        | 1             | 0                        |
| Open           | Open                          | 0             | 1                        | 0             | 1                        |
| Closed         | Closed                        | 0             | 1                        | 1             | 1                        |
| Open           | Open                          | 1             | 1                        | 0             | 0                        |
| Closed         | Open                          | 1             | 1                        | 1             | 0                        |
| Closed         | Open                          | 1             | 1                        | 0             | 1                        |
| Closed         | Open                          | 1             | 1                        | 1             | 1                        |

Figure 4

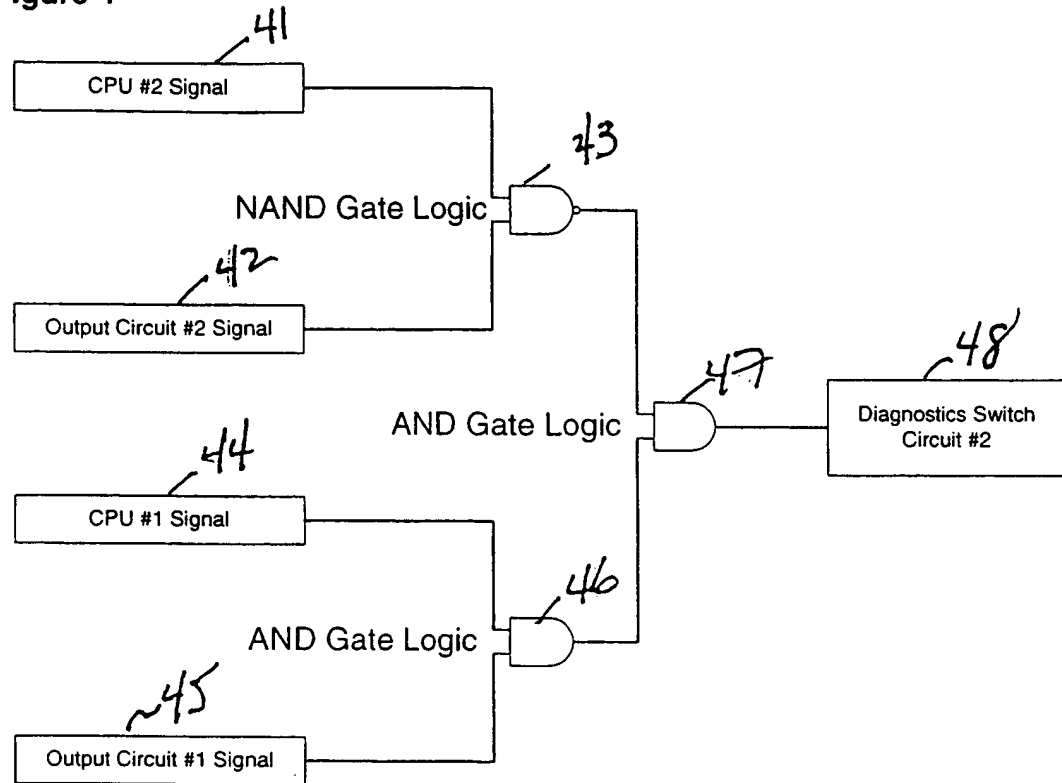


Figure 5

| Pressure Switch Output Circuit | Diagnostics Switch Circuit #2 | CPU #2 Signal | Output Circuit #2 Signal | CPU #1 Signal | Output Circuit #1 Signal |
|--------------------------------|-------------------------------|---------------|--------------------------|---------------|--------------------------|
| Open                           | Open                          | 0             | 0                        | 0             | 0                        |
| Open                           | Open                          | 0             | 0                        | 1             | 0                        |
| Open                           | Open                          | 0             | 0                        | 0             | 1                        |
| Open                           | Open                          | 0             | 0                        | 1             | 1                        |
| Open                           | Open                          | 1             | 0                        | 0             | 0                        |
| Open                           | Open                          | 1             | 0                        | 1             | 0                        |
| Open                           | Open                          | 1             | 0                        | 0             | 1                        |
| Closed                         | Closed                        | 1             | 0                        | 1             | 1                        |
| Open                           | Open                          | 0             | 1                        | 0             | 0                        |
| Open                           | Open                          | 0             | 1                        | 1             | 0                        |
| Open                           | Open                          | 0             | 1                        | 0             | 1                        |
| Closed                         | Closed                        | 0             | 1                        | 1             | 1                        |
| Open                           | Open                          | 1             | 1                        | 0             | 0                        |
| Closed                         | Open                          | 1             | 1                        | 1             | 0                        |
| Closed                         | Open                          | 1             | 1                        | 0             | 1                        |
| Closed                         | Open                          | 1             | 1                        | 1             | 1                        |